PLEASE ENTER TM 05/12/2006

IN THE CLAIMS

1 (Currently Amended).   A method comprising:

> providing a register accessible by a plurality of central processing units; and

> indicating whether data in said register is available for a given central processing unit by providing different indicators assigned to each of a plurality of central processing units and ~~enabling~~ causing the given central processing unit to reset its indicator when the data in said register is no longer useful to the given central processing unit.

2 (Previously Presented).   The method of claim 1 including indicating for each of a plurality of central processing units whether the data is available for a given central processing unit.

3 (Previously Presented).   The method of claim 2 including requiring a central processing unit to wait to execute an instruction until the data it needs to execute the instruction is available in one or more registers.

4 (Previously Presented).   The method of claim 3 including providing a bit for each item of data indicating whether a given central processing unit can access that data.

5 (Previously Presented).   The method of claim 4 including resetting said bit when said data is accessed by a given central processing unit.

6 (Previously Presented).   The method of claim 1 including preventing any central processing unit from writing data to said register until all of the indicators for the plurality of central processing units indicate that the data is no longer useful to any other central processing unit.

7 (Previously Presented).   The method of claim 6 including indicating the central processing unit which will utilize the data written into the register.

2